## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re PATENT APPLICATION Of:

**Applicants** Yoshimi EGAWA

Serial No. To Be Assigned

INFORMATION Filed November 28, 2003 **DISCLOSURE STATEMENT** 

For SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF

Attorney Ref.: **OKI 388** 

November 28, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of two Japanese publications. Any relevance of the Japanese publications can be gleaned from the attached English-language Abstracts. The publications are listed on the attached Form PTO-1449 and discussed on page 2 of the application. Also listed on the form PTO-1449 for consideration are one U.S. Patent and one U.S. Patent Application Publication. As the present application was filed after June 30, 2003, no copies of the U.S. Patent documents are submitted with the IDS, pursuant to the Notice of U.S. Patent & Trademark Office posted on the USPTO website on July 11, 2003.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted and listed documents is respectfully requested.

Respectfully submitted,

November 28, 2003

Date

Steven M. Rabin (Reg. No. 29,102)

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